



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IFW  
2526

Docket No.: AMD-G0188

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the below date of deposit.

Date of Deposit:	03/02/05	Name of Person Making the Deposit:	Judy Davenport	Signature of the Person Making the Deposit:	<i>Judy Davenport</i>
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Inventor(s): Zhigang Wang, Nian Yang, and Yue-Song He

Serial No.: 09/917,440

Group Art Unit: 2826

Filed: 07/27/01

Examiner: Quinto, Kevin V.

Confirmation No: 9710

Title: AN N-GATE/N-SUBSTRATE OR P-GATE/P-SUBSTRATE CAPACITOR TO CHARACTERIZE POLYSILICON GATE DEPLETION EVALUATION

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

TRANSMITTAL OF FORMAL DRAWINGS

In response to Drawing Informalities

attached please find:

☒ (a) the formal drawings for this application  
Number of Sheets 12

Each sheet of drawing indicates the identifying indicia suggested in § 1.84(c) on the reverse side of the drawing

☐ (b) a copy of the NOTICE OF INFORMAL DRAWINGS

Please direct all correspondence concerning the above-identified application to the following address:

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Respectfully submitted,

Date:

3/2/05

By:

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